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Attorney Docket no: SEM 137 DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

APR -1 2003

Yamazaki et al.

TECHNOLOGY CENTER 2800

Serial No.: 09/821,473

Filed: March 29, 2001

For: Semiconductor Device Provided With
Semiconductor Circuit Made of
Semiconductor Element And Method Of
Fabricating The Same

Examiner: M. Lee

Art Unit: 2823

I hereby certify that this correspondence is being
deposited with the United States Postal Service as
first class mail in an envelope addressed to:
the Assistant Commissioner for Patents, Washington,
D.C. 20231 on January 7, 2003
(Date of Deposit)

Rachelle Hammarquist
Name of applicant, assignee, or Registered Rep.

[Signature] 1-7-03
Signature Date

#20 / 8-26-03
FDS
RTH

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Pursuant to 37 C.F.R. §1.97, as revised on February 4, 1992, 1135 OG 23-24, Applicant hereby call the Examiner's attention to documents listed on the attached form, which documents may be material to the examination of this application. A copy of each of the documents is enclosed via first class mail for the Examiner's consideration.

No inference should be drawn that any apparatus disclosed is equivalent to the subject invention.

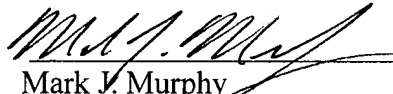
The citation of the above-discussed documents is not to be construed as an assertion that more pertinent art could not possibly be in existence. Citation of any document herein is not to be construed as an admission that any subject matter disclosed in the document is necessarily within the inventive field of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

As Applicants are filing this IDS with an RCE, no fee is believed to be necessary. However, if such a fee is due, please charge our Deposit Account No. 50/1039 for the fee or any deficiency.

Respectfully submitted,

Date: January 7, 2002


Mark J. Murphy
Registration No. 34,225

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LIST OF PUBLICATIONS/MATERIALS CITED BY APPLICANT		<u>Attorney Docket No.</u> SEL 137 DIV		
		<u>Applicant</u> Yamazaki et al.		
		<u>Filing Date</u> March 29, 2001		
U.S. PATENT DOCUMENTS				
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	FILING DATE
	6,143,661	11/7/00	Kousai et al.	8/13/98
	6,348,369	2/19/02	Kusumoto et al.	9/3/99
	6,482,752	11/19/02	Yamazaki et al.	7/24/96
FOREIGN PATENT DOCUMENTS				
	DOCUMENT NUMBER	DATE	COUNTRY	FILING DATE

OTHER PUBLICATIONS/MATERIALS (Including Author, Title, Date, Pertinent Pages)	
	Hayashi et al., "Fabrication Of Low-Temperature Bottom-Gate Poly-Si TFTs on Large-Area Substrate by Linear-Beam Excimer Laser Crystallization and Ion Doping Method", IEDM 95, p. 33.3.1 - 33.3.4 (1995)

EXAMINER:	DATE CONSIDERED:
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.	